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APPLICANTS Jing-Horng Gau, Hsin-Chu Hsien, TAIWAN; <i>506</i>					
** CONTINUING DATA ***** <i>NONE</i>					
** FOREIGN APPLICATIONS ***** <i>NONE</i>					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 03/11/2004					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Verified and <i>None</i> Acknowledged <i>506</i> Examiner's Signature Initials		STATE OR COUNTRY TAIWAN	SHEETS DRAWING 24	TOTAL CLAIMS 19	INDEPENDENT CLAIMS 2
ADDRESS 027765 (NAIPC) NORTH AMERICA INTERNATIONAL PATENT OFFICE P.O. BOX 506 MERRIFIELD, VA 22116					
TITLE METHOD OF FABRICATING A BIPOLAR JUNCTION TRANSISTOR					
FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____		